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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Lee et al.

Attorney Docket No.: EICCP011X1

Application No.: 10/820,343

Examiner: Not Yet Assigned

Filed: April 7, 2004

Group: 2811

Title: HETEROJUNCTION BIPOLAR  
TRANSISTOR HAVING NON-UNIFORMLY  
DOPED COLLECTOR FOR IMPROVED SAFE-  
OPERATING AREA

Conformation No.: 8156

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on June 23, 2004 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: \_\_\_\_\_

  
Laura M. Dean

**INFORMATION DISCLOSURE STATEMENT  
37 CFR §§1.56 AND 1.97(b)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

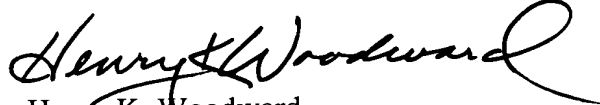
The references listed in the attached PTO Form 1449 may be material to examination of the above-identified patent application. Applicants submit the list of these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application. The above-identified application is a Continuation-in-Part of prior application U.S. Patent Application No. 10/267,215. This prior application is being relied upon for an earlier filing date under 35 U.S.C. § 120. Because the listed references were either cited by the PTO, or submitted to the PTO in the prior application, under 37 CFR § 1.98(d) Applicants submit that copies need not be provided.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. EICCP011X1 ).

Respectfully submitted,

BEYER WEAVER & THOMAS, LLP

A handwritten signature in black ink, appearing to read "Henry K. Woodward", with a large, sweeping flourish at the end.

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<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Atty Docket No. EICCP011X1	Application No.: 10/820,343
	Applicant: Lee et al.	
	Filing Date 04/07/2004	Group 2811

#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date

#### Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
							Yes	No

#### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
X	A	Fullowan et al., "Improved Breakdown of AlInAs/InGaAs Heterojunction Bipolar Transistors", Electronics Letters, Vol. 27, pp. 2340-2341, December 1991.
✓	B	Hassan et al., "Control of Current-Mode Second Breakdown in Transistors Through Use of Double-Graded Collectors", Solid-State Electronics, Vol. 33, No. 10, pp. 1217-1221, 1990.
X	C	Pfost et al., "Optimization of the Collector Profile of InGaP/GaAs HBTs for Increased Robustness", 25 <sup>th</sup> IEEE GaAsIC Symposium, Nov. 9-12, pp. 115-118, 2003.
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

x Did not receive